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\*\* CONTINUING DATA *None NL* \*\*\*\*\*

\*\* FOREIGN APPLICATIONS *None NL* \*\*\*\*\*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
Verified and Acknowledged	<i>None</i> Examiner's Signature	<i>NL</i> Initials			

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TITLE  
 Method and apparatus for power optimization during an integrated circuit design process

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